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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/753,764	
	Filing Date	December 29, 2000	
	First Named Inventor	Sailesh B. KOTTAPALLI	
	Art Unit	2183	
	Examiner Name	David J. HUISMAN	
Total Number of Pages in this Submission	19	Attorney Docket Number	Intel 2207/10122

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
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Printed Name	Stephen T. Neal		
Date	April 13, 2005	Reg. No.	47,815

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Attorney Docket No.: Intel 2207/10122
Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Sailesh B. KOTTAPALLI
SERIAL NO. : 09/753,764
FILED : December 29, 2000
FOR : METHOD AND APPARATUS FOR REDUCING
COMPONENTS NECESSARY FOR INSTRUCTION POINTER
GENERATION IN A SIMULTANEOUS MULTITHREADED
PROCESSOR
GROUP ART UNIT : 2183
EXAMINER : David J. HUISMAN

M/S: AMENDMENT
Commissioner for Patents
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Dated: April 13, 2005	Signature <u>Barbara Vance</u> Barbara Vance

AMENDMENT AND RESPONSE

Dear Sir:

The following amendment and remarks below are respectfully submitted in response to the Office Action dated January 13, 2005 and the interview with Examiner David J. Huisman on April 5, 2005.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begin on page 3 of this paper.

Amendments to the Drawings begin on page 9 of this paper and include both attached replacement sheets and annotated sheets showing change.

Remarks/Arguments begin on page 10 of this paper.

An **Appendix** including amended drawings is attached after page 13 of this paper.